Notice of References Cited

Application/Control No. 10/588,191	Applicant(s)/Patent Under Reexamination KINOSHITA ET AL.	
Examiner	Art Unit	
BRANDON S. COLE	4125	Page 1 of 1

II S PATENT DOCUMENTS

	U.S. PATENT DOCUMENTS				
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,201,435	03-2001	Chou, Yung-Fa	327/543
*	В	US-6,891,433	05-2005	Schrader, Victor Paul	330/253
	С	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-		-	
	Н	US-			
		US-			
	J	US-			
	К	US-			
	L	US-			
	м	US-			

FOREIGN PATENT DOCUMENTS

	FOREIGN FATENT DOCUMENTS					
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	s					
	Т	100				

NON-PATENT DOCUMENTS

	HONT ALERT DOCUMENTO			
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	Design and Analysis of Integrator-Based Log-Domain Filter Circuits by Gordon W. Roberts, Vincent W. Leung http://books.google.com/books?dd=g4A4yTP- TZ4C&pg=PA2&lpg=PA2&dq=transistor+acting+as+resistor&source=web&ots=rM3nE3NmCA&sig=hkD0mF3pX8NWFsBtQbwstGLCfQ#PFA3,M1		
	V			
	w			
	×			

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